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whc 2/2/05

Docket No.: M4065.0340/P340
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Graham Kirsch

Application No.: 09/652,003　　　　　　　　Art Unit: 2186

Filed: August 31, 2000　　　　　　　　　　Examiner: W. H. Choi

For:　METHOD AND APPARATUS FOR
　　　CONNECTING A MASSIVELY PARALLEL
　　　PROCESSOR ARRAY TO A MEMORY
　　　ARRAY IN A BIT SERIAL MANNER

AMENDMENT AFTER FINAL ACTION (37 C.F.R. SECTION 1.116)

MS AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA　22313-1450

Dear Sir:

INTRODUCTORY COMMENTS

In response to the Office Action dated July 22, 2004 (Paper No. 19), finally rejecting claims 1-6, 11-13, 22-27, 33-35, 41, 43-49 and 51, and to the Advisory Action dated December 22, 2004, please amend the above-identified U.S. patent application as follows:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 23 of this paper.